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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Date of Signature

In the Appli	cation of)	NT / A
DAN TOMESCU, et al.) Examiner:) Group Art Unit:	Not Assigned 2183
HAV	A PROCESSING SYSTEM ING A CARTESIAN FROLLER)))	
Serial No.:	10/728,234)	
Filed on:	December 4, 2003) (Our Docket No.	5227-0001-1)
Mail Stop A	mendment ONER FOR PATENTS	·	

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sirs:

P.O. Box 1450

Alexandria, VA 22313-1450

In accordance with 37 C.F.R. § 1.56, the references set forth below and listed on the attached forms PTO/SB/08A and PTO/SB/08B are being brought to the attention of the Examiner for consideration in connection with the examination of the above-identified patent application. Copies of the cited references are enclosed.

U.S. Patent No	Entitled	<u>Dated</u>
5,758,176	Method and System for Providing a Single-Instruction, Multiple-Data Execution Unit for Performing Single-Instruction, Multiple-Data Operations Within a Superscalar Data Processing System	05-26-1998

Intl Publ. No Entitled Dated

WO 01/53933 Split Embedded Dram Processor 07-26-2001

OTHER DOCUMENTS

SHAW D E et al: "Active Memory Chip: A Brief Survey And Case Study" Circuits, Systems And Signal Processing, Cambridge, MS, US, vol. 6 no. 2, 1987 pages 239-258, XP000747239, ISSN: 0278-081X

This statement is not to be interpreted as a representation that the cited publications are material, that an exhaustive search has been conducted, or that no other relevant information exists. Nor shall the citation of any publication herein be construed *per se* as a representation that such a publication is prior art. Moreover, the Applicant understands that the Examiner is to make an independent evaluation of the cited information.

It is respectfully requested that the Examiner indicate consideration of the cited references by returning a copy of the attached forms PTO/SB/08A and PTO/SB/08B with initials or other appropriate marks.

No fees are considered to be due; however, if it is determined that payment of a fee is required, please charge our deposit account No. 13-0235.

Respectfully submitted,

Nicholas J. Tuccillo, Esq.

Attorney for Applicant(s) Registration No. 44,322

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PTO/SB/08A (07-05)

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INFORMATION	DISCLOSURE
STATEMENT B	Y APPLICANT

or form 1449/PTO

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Sheet

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of

Complete if Known			
Application Number	10/728,234		
Filing Date	December 4, 2003		
First Named Inventor	DAN TOMESCU		
Art Unit	2183		
Examiner Name			
Attorney Docket Number	5227-0001-1		

			U. S. PATENT	DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ^{2 (f known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages. Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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Examiner Initials*	Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages	
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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden. should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450. Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

PTO/SB/08B (07-05)

5227-0002-1

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Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number. Complete if Known Substitute for form 1449/PTO **Application Number** 10/728,234 INFORMATION DISCLOSURE **Filing Date** December 4, 2003 STATEMENT BY APPLICANT **First Named Inventor** DAN TOMESCU Art Unit 2183 (Use as many sheets as necessary) **Examiner Name**

Attorney Docket Number

Sheet

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	NON PATENT LITERATURE DOCUMENTS	
Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	SHAW D E et al: "Active Memory Chip: A Brief Survey And Case Study" Circuits, Systems And Signal Processing, Cambridge, MS, US, vol. 6 no. 2, 1987 pages 239-258, XP000747239,	
	Cite No.1	Cite No. Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. SHAW D E et al: "Active Memory Chip: A Brief Survey And Case Study" Circuits, Systems And Signal Processing, Cambridge, MS, US, vol. 6 no. 2, 1987 pages 239-258,

Examiner Date Signature Considered

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Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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